

## **Statement of Volatility - Latitude 3550**

△ CAUTION: A CAUTION indicates either potential damage to hardware or erasure of data and tells you how to avoid the problem.

The Latitude 3550 contains both volatile and non-volatile components. Volatile components erase their data immediately after power is removed from the component. Non-volatile components continue to retain their data even after power is removed from the component. The following non-volatile components are present on the Latitude 3550 system board.

Table 1. List of non-volatile components on system board

Reference Designator  SSD1- M.2 2230  BIOS1 (32 MB)- Non Pro for RPL  BIOS1+BIOS2 (32 MB+16 MB)- Non vPro or MTL  J2401  J7103  J7204  Part of panel assembly	Non-Volatile memory, Video BIOS for basic boot operation, PSA (on board diags), PXE diags. Non-Volatile memory, 192 KB of ROM Non-Volatile memory, 8 Mbit (1 MB) (Burnside-Bridge FW) for RPL Non-Volatile memory, 8 Mbit (1 MB) (Hayden-Bridge FW) for MTL Non-Volatile memory, 64 KB ROM for USB Type-C PD F/W Non-Volatile memory, Stores panel manufacturing information, display configuration data	No No No No No	NA NA NA
BIOS1 (32 MB)- Non Pro for RPL BIOS1+BIOS2 (32 MB+16 MB)- Non vPro or MTL J2401 J7103	Non-Volatile memory, Video BIOS for basic boot operation, PSA (on board diags), PXE diags. Non-Volatile memory, 192 KB of ROM Non-Volatile memory, 8 Mbit (1 MB) (Burnside-Bridge FW) for RPL Non-Volatile memory, 8 Mbit (1 MB) (Hayden-Bridge FW) for MTL Non-Volatile memory, 64 KB ROM for USB Type-C PD F/W Non-Volatile memory, Stores panel manufacturing information, display	No No	NA NA
BIOS1 (32 MB)- Non Pro for RPL BIOS1+BIOS2 (32 MB+16 MB)- Non vPro or MTL J2401 J7103	Non-Volatile memory, Video BIOS for basic boot operation, PSA (on board diags), PXE diags. Non-Volatile memory, 192 KB of ROM Non-Volatile memory, 8 Mbit (1 MB) (Burnside-Bridge FW) for RPL Non-Volatile memory, 8 Mbit (1 MB) (Hayden-Bridge FW) for MTL Non-Volatile memory, 64 KB ROM for USB Type-C PD F/W Non-Volatile memory, Stores panel manufacturing information, display	No No	NA NA
Pro for RPL  BIOS1+BIOS2 (32  MB+16 MB)- Non vPro or MTL  J2401  J7103	BIOS for basic boot operation, PSA (on board diags), PXE diags.  Non-Volatile memory, 192 KB of ROM  Non-Volatile memory, 8 Mbit (1 MB) (Burnside-Bridge FW) for RPL  Non-Volatile memory, 8 Mbit (1 MB) (Hayden-Bridge FW) for MTL  Non-Volatile memory, 64 KB ROM for USB Type-C PD F/W  Non-Volatile memory, Stores panel manufacturing information, display	No No	NA NA
MB+16 MB)- Non vPro or MTL J2401 J7103 J7204	BIOS for basic boot operation, PSA (on board diags), PXE diags.  Non-Volatile memory, 192 KB of ROM  Non-Volatile memory, 8 Mbit (1 MB) (Burnside-Bridge FW) for RPL  Non-Volatile memory, 8 Mbit (1 MB) (Hayden-Bridge FW) for MTL  Non-Volatile memory, 64 KB ROM for USB Type-C PD F/W  Non-Volatile memory, Stores panel manufacturing information, display	No No	NA NA
J7204	PSA (on board diags), PXE diags.  Non-Volatile memory, 192 KB of ROM  Non-Volatile memory, 8 Mbit (1 MB) (Burnside-Bridge FW) for RPL  Non-Volatile memory, 8 Mbit (1 MB) (Hayden-Bridge FW) for MTL  Non-Volatile memory, 64 KB ROM for USB Type-C PD F/W  Non-Volatile memory, Stores panel manufacturing information, display	No No	NA NA
J2401 J7103 J7204	Non-Volatile memory, 192 KB of ROM Non-Volatile memory, 8 Mbit (1 MB) (Burnside-Bridge FW) for RPL Non-Volatile memory, 8 Mbit (1 MB) (Hayden-Bridge FW) for MTL Non-Volatile memory, 64 KB ROM for USB Type-C PD F/W Non-Volatile memory, Stores panel manufacturing information, display	No No	NA NA
J7103 J7204	ROM  Non-Volatile memory, 8 Mbit (1 MB) (Burnside-Bridge FW) for RPL  Non-Volatile memory, 8 Mbit (1 MB) (Hayden-Bridge FW) for MTL  Non-Volatile memory, 64 KB ROM for USB Type-C PD F/W  Non-Volatile memory, Stores panel manufacturing information, display	No	NA
J7103 J7204	Non-Volatile memory, 8 Mbit (1 MB) (Burnside-Bridge FW) for RPL  Non-Volatile memory, 8 Mbit (1 MB) (Hayden-Bridge FW) for MTL  Non-Volatile memory, 64 KB ROM for USB Type-C PD F/W  Non-Volatile memory, Stores panel manufacturing information, display	No	NA
J7204	MB) (Burnside-Bridge FW) for RPL  Non-Volatile memory, 8 Mbit (1 MB) (Hayden-Bridge FW) for MTL  Non-Volatile memory, 64 KB ROM for USB Type-C PD F/W  Non-Volatile memory, Stores panel manufacturing information, display		
J7204	MB) (Hayden-Bridge FW) for MTL  Non-Volatile memory, 64 KB ROM for USB Type-C PD F/W  Non-Volatile memory, Stores panel manufacturing information, display		
J7204	MB) (Hayden-Bridge FW) for MTL  Non-Volatile memory, 64 KB ROM for USB Type-C PD F/W  Non-Volatile memory, Stores panel manufacturing information, display		
	for USB Type-C PD F/W  Non-Volatile memory, Stores panel manufacturing information, display	No	NA
	Non-Volatile memory, Stores panel manufacturing information, display	No	NA NA
art of panel assembly	manufacturing information, display		
Part of panel assembly			i contract of the contract of
	I configuration data	No	NA
60-DIMM DDR5 *2 Slot:			
DM1/DM2	Volatile memory in OFF state	No	NA
NDLI1 (DOLI)		No	NIA
For RPL/MTL UMA	Stores CMOS III/O/Mation	INO	NA
latform: Using system			
nemory			
or RPL DIS	Volatile memory in off state		
SN20-S5L	UMA uses main system memory size		
/RAM1 / VRAM2	allocated out of main memory.	No	N/A
	Non-Volatile memory, 2 MB of		
1/902		No	N/A
Combine on BIOS ROM	security and protection	No	N/A
J9101	Non-Volatile memory, 2 KB ROM	No	N/A
Combine on		NIs	N1/A
SIUS RUM		NO	N/A
I/A	Non-Volatile memory	No	N/A
PU4601 for RPL IMVP9.1	N. V. I. III		
'U4601 tor M	Non-Volatile memory	No	N/A
IA	Non-Volatile memory	No	NA
	D-DIMM DDR5 *2 Slot: M1/DM2  PU1 (PCH) or RPL/MTL UMA atform: Using system nemory or RPL DIS N20-S5L RAM1 / VRAM2  Dombine on BIOS ROM  9101  ombine on IOS ROM	D-DIMM DDR5 *2 Slot: M1/DM2  Non-Volatile memory in OFF state  Non-Volatile memory 256 bytes Stores CMOS information  Volatile memory in off state. Stores CMOS information  Volatile memory in off state. UMA uses main system memory size allocated out of main memory.  Non-Volatile memory, 2 MB of ROM for RPL  Non-Volatile memory, Intel ME firmware for system configuration, security and protection  Non-Volatile memory, 2 KB ROM  Non-Volatile memory, 2 KB ROM  Non-Volatile memory, 2 KB ROM  Non-Volatile memory  Non-Volatile memory  Non-Volatile memory  Non-Volatile memory  Non-Volatile memory	D-DIMM DDR5 *2 Slot: M1/DM2  Volatile memory in OFF state  No  Non-Volatile memory 256 bytes Stores CMOS information  No  PU1 (PCH) Stores CMOS information  No  RPL/MTL UMA atform: Using system emory  POT RPL DIS N20-S5L RAM1 / VRAM2  Volatile memory in off state. UMA uses main system memory size allocated out of main memory.  Non-Volatile memory, 2 MB of ROM for RPL  No  Non-Volatile memory, Intel ME firmware for system configuration, security and protection  No  P101  Non-Volatile memory, 2 KB ROM  No  Non-Volatile memory, 2 KB ROM  No  Non-Volatile memory, No

Finger printer FW	NA	Non-Volatile memory	No	NA
Touch Pad FW	NA	Non-Volatile memory	No	NA

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CAUTION: All other components on the system board erase data if power is removed from the system. Primary power loss (unplugging the power cord and removing the battery) destroys all user data on the memory (DDR5, 5200 MHz (for computers shipped with13th Gen Intel Core i3/i5/i7 Processors)/ DDR5, 5600 MHz (for computers shipped with Intel Core Ultra i5/i7processors)). Secondary power loss (removing the on-board coin-cell battery) destroys system data on the system configuration and time-of-day information.

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